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RESEARCH ARTICLE

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Simplified SVPWM for Three-Level Inverter Extending Operation into Over Modulation Region

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ABSTRACT

A three-level inverter operated in over modulation region, gives higher speed, higher torque and higher power characteristics. In comparison to a two-level inverter, a three-level inverter has a larger number of switches and is more difficult to implement in the over modulation zone. In this paper, a simplified space vector PWM is proposed for over-modulation region of space vector modulated three level inverter fed induction motor drive. In this method timings are calculated for sector A and the timings for remaining sectors are calculated using mapping, hence number of calculations is reduced and implementation is simplified. The proposed method is verified using MATLAB simulation. In this study, the SVPWM is used in three different regions, including overmodulation region 1 and region 2, and the outcomes are compared.

Keywords—over modulation; space vector; linear modulation;

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I. INTRODUCTION

THREE-LEVEL voltage-fed PWM inverters are recently showing growing popularity for multimegawatt industrial drive applications. Sharing of large voltage between the series devices and improvement of harmonic quality at the output compared to a two-level inverter. The rapid development of the capacity and the switching frequency of power semiconductor devices and the continuous advance of the power electronics technology have made many changes in static power converter systems and industrial motor drive areas. Especially, the voltage source PWM inverters have been extending their application area widely due to the increase of gate turn-off (GTO) thyristors capacity and continuing development of industrial provision.

Three-level inverter topology being widely used in high voltage/ high power applications due to its high voltage handling and good harmonic rejection capabilities with currently available power devices like GTOS. Fig.1 shows the basic circuit diagram of three-level inverter excluding detailed snubber circuits and anti parallel diodes. It is known that the three-level inverter roughly has four times better in harmonics content compared with conventional two-level topology having same number of devices and ratings. So far various PWM techniques controlling three-level inverter have been studied and a good plenty of results are published such as modified two-level triangular carrier modulation, cost function minimizing PWM and space vector. Space vector PWM of a three-level inverter is considerably more complex than that of a two-level inverter because of large number of inverter switching states. In addition, there is the problem of neutral point voltage balancing. Unfortunately, operation in the under modulation range of SVM restricts the drive operation in constant torque region only with voltage modulation factor up to 90.7%. An over modulation strategy of SVM with modulation factor extending from 90.7% to near unity is essential if the drive is required to operate at extended speed including the field-weakening region in vector control with higher torque and power characteristics.

II. SIMPLIFIED SVPWM IN UNDERMODULATION REGION

A. *Three-Level Topology and switching states* A three-level GTO inverter is shown schematically in Fig1 . Two clamping diodes, four GTOs, and four freewheeling diodes make up each phase of the inverter. When the switches S_{1A} and S_{2A} are closed, the phase V is in state P (positive bus voltage), and when S_{3A} and S_{4A} are closed, it is in state N (negative bus voltage). When the phase is clamped at the neutral point, the phase is in condition O, and either S_{2A} or S_{3A} conducts depending on the positive or negative phase current polarity. . The three level inverter has $27(3^3)$ switching states since each phase has three types of switching states and terminal voltages. The space voltage vectors for output voltage are presented in Fig.2. We divide the min into four categories based on the size of the voltage vectors: zero voltage vector (Vo), small voltage vectors (V1, V4, V7, V10, V13, V16), moderate voltage vectors (V1, V4, V7, V10, V13, V16) (V3, V6, V9, V12 ,V15, V18), and Large voltage vector (V_2 , V_5 , V_8 , V_{11} , V₁₄, V₁₇) where the P, O, N represent terminal voltage respectively, that is Vdc/2, 0, -Vdc/2.



Fig.1. Circuit diagram of 3L NPC inverter



Fig.2. Space vector diagram for Three-Level inverter

B. Voltage Vector and its Duration

The triangle produced by the voltage vectors V0, V2, and V5 is shown in Fig.3. There are four little triangles in this triangle: 1, 2, 3, and 4. In order to minimise the harmonic components of the output voltage and current, the output voltage vector in space voltage vector PWM is often produced by its nearest three vectors. Vector calculation can be used to determine the duration of each vector. The duration of each voltage vector can be estimated using the following equations if the reference voltage vector falls into the triangle 2.



$$V_{1}T_{a}+V_{3}T_{b}+V_{4}T_{c}=V^{*}T_{s}$$
(1)

$$T_{a}+T_{b}+T_{c}=T_{s}$$
(2)

The on of the voltage vectors can be estimated using the formulae above :

(4)

$$T_{a} = T_{s} [1-2k \sin(\theta)]$$

$$T_{b} = T_{s} [2k \sin(\theta+60)-1] (5)$$

$$T_{b} = T_{s} [2k \sin(\theta+60)-1] (5)$$

 $T_{c=} T_{s} [2k \sin(\theta - 60) + 1]$ (6) Where $k = 2V/\sqrt{3}$ The dwelling time in other

where $\kappa = 2r/r^{3}$ The dwelling time in other regions of sector A can be calculated using the same technique, as shown in Table I.

Table I dwelling times for all regions in sector A

	¥	¥	
Region	Ta	T _b	T _c
1	$2kT_s \sin(60-\theta)$	$T_s[1-2ksin(\theta+60)]$	$2kT_s \sin(60-\theta)$
2	2T _s [1-	2kT _s sinθ	$T_s[2ksin(60-\theta)-$
	$ksin(\theta+60)$]		1]
3	T _s [1-2ksinθ]	$T_s[2ksin(\theta+60)-1]$	$T_s[2ksin(\theta -$
			60)+1]
4	$T_s[2ksin\theta-1]$	$2kT_s \sin(60-\theta)$	2T _s [1-
			$ksin(\theta+60)$]

C. Arrangement for Switching Sequences

After calculating the on times, the switching sequence must be defined. The converter, however, has some redundant switching states, so there are a few possibilities to choose from. Switching sequence can be arranged according to a specific optimal goal, such as lowest switching loss or total harmonic distortion (THD). In order to achieve reduced THD, every relevant switching is addressed in this study. The switching sequence is made up of states. The Sector A switching sequences are organized as follows[1]:

Region 1: PPO-POO-OOO-OON-ONN and return Region 2: PPO-POO-PON-OON-ONN and return Region 3: PPO-PPN-PON-OON and return Region 4: POO-PON-PNN-ONN and return

(

D. Time setting for each switch

Switching sequence for region 2 is shown in Fig 4. As shown in Table II, the time settings for each switch in sector A can be obtained.

	I able II				
Region	1	2	3	4	
S1a	Ta/4+Tc/	Ta/4+Tb/2	Ts/2-Tc/4	Ts/2-	
	4	+Tc/4		Ta/4	
S2a	Ts/2	Ts/2	Ts/2	Ts/2	
S1b	Tc/4	Tc/4	Tc/4+Ta/2	0	
S2b	Ts/2-Ta/4	Ts/2-Ta/4	Ts/2	Ta/4+T	
				b/2	
S1c	0	0	0	0	
S2c	Ts/2-	Tc/4+Ta/4	Tc/4	Ta/4	
	Ta/4-Tc/4				

T 11 T

The procedures for realizing the above results in sector A can be applied to other sectors with similar results. Figure 4 depicts the calculation flow for the three-level SVPWM. Any reference vector that remains in any position has the same flow, although the voltage vectors and switching patterns are different. Each region has its own set of sequences. So there are 24 routines in total. To complete the three-level SVPWM, you'll need (one procedure for each region).



Fig.4 Conventional calculation procedure for three level SVPWM

The fundamental inspiration for the simpler approach comes from the fact that the shapes of the six sectors are identical (see Fig. 2). As a result, there should be some close correlations between on-time computations and switch arrangements in each step. We can calculate the on times for switches in a given sector and then map the on times in that sector to the corresponding on times in other sectors using the relationships between them if the linkages between them can be clearly established.

When the reference vector in other sectors is rotated by $n\pi/3$ (n=1, 2, 3, 4, 5) to sector A, the on times in sector A are equivalent to those in other sectors.

The switching sequences can be arranged in sector A and then mapped to the other sectors if the relationship between the switching sequences in sector A and those in other sectors is drawn by shifting and reversing phase sequences. In other sectors, the relevant reference vector can be created as shown in Table III.

Table III				
Sector	Phase	Phase	Phase	
	voltage	voltage	voltage	
	А	В	С	
А	Ua	U _b	U _c	
В	-U _b	-U _c	-U _a	
С	Uc	Ua	U _b	
D	-U _a	-U _b	-U _c	
E	U _b	Uc	Ua	
F	-U _c	-U _a	-U _b	

Simplified calculation procedure is shown in the Fig 5



Fig .5. simplified calculation procedure for three level **SVPWM**

SVM OPERATION EXTENDING INTO III. **OVERMODULATION REGION**

When the reference voltage V* surpasses the hexagon border, the inherently nonlinear operation in the overmodulation zone begins. The three-level inverter's SVM overmodulation approach is identical to that of a two-level inverter. Because the theory of overmodulation is the same in all sectors, we will just discuss the procedure for sector A. V* crosses the hexagon side at two spots in overmodulation mode-1, as illustrated in Fig.6 for sector A. To compensate for the loss of fundamental voltage, i.e., to match the output fundamental voltage to the reference voltage, a modified reference voltage trajectory is used that is partly hexagonal and partially circular. The circular component of the trajectory, represented by the segments ab and de, has a larger radius Vm^* ($Vm^* > V^*$) and intersects the hexagon at an angle, as indicated in the diagram. Vm^* can be expressed as a function of crossover angle [3] as follows:

$$V_m^* = \frac{2V_d(\frac{\pi}{6} - \theta)}{\pi \sin(\frac{\pi}{6} - \theta)}$$

where Vd is the voltage across the dc link The under modulation method, as previously discussed, is still viable in the circular segment. In the ab segment, for example, the inverter vectors V1,V2, and V3 are chosen for the associated time segments Ta,Tb, and Tc. Similarly, for the de segment, the vectors V4,V3, and V5 are chosen, with Tc,Tb, and Ta being the time segments. On the hexagon, the trajectory segment bd can be separated into two parts: bc and cd. Only the vectors V2, and V3 are selected for the corresponding time segments Tc and Tb in the bc segment, whereas the vectors V3 and V5 are picked for time segments Tb and Ta in the cd segment.

When V*or m rises any higher, the overmodulation mode-2 kicks in. Partly keeping the hexagon corner vector for holding angle h and partly tracking the hexagon side (segments bc and cd) in every sector characterizes the operation in this region, as seen in Fig. 7. During holding angle, the machine phase voltages remain constant, however during hexagon tracking, the voltages fluctuate in a manner similar to mode-1. From Fig. 7, the formula for modified angle α and (α_m) can be written as

$$\alpha_m^* = 0 \quad for \ 0 < \alpha^* < \alpha_h$$
$$\alpha_m^* = \frac{\alpha^* - \alpha_h}{\frac{\pi}{6} - \alpha_h} \quad for \ 0 < \alpha^* < \frac{\pi}{3} - \alpha_h$$
$$\alpha_m^* = \frac{\pi}{3} \quad for \ \frac{\pi}{3} - \alpha_h < \alpha^* < \frac{\pi}{3}$$

In order to maintain a three-level output voltage wave, the greatest modulation factor (m) of a three-level inverter should be kept to slightly less than one $(1-\varepsilon)$.



A. Time calculations for space vectors

As previously stated, a major portion of operation occurs on the hexagon side in the sections bc in region 2 and cd in region 4 during mode 1 of the overmodulation area. These sections' analytical formulations for time segments will now be generated. Consider the case where the reference voltage V* follows the section bc in area 2 as shown in Fig. 6. The general trajectory angle θ_e has taken the place of the sector angle. For space vector PWM, the following two equations should be valid:

$$V_2 * Tb + V_3 * Tc = V^* * \frac{Ts}{2}$$
(3)
$$Tc + Tb = \frac{Ts}{2}$$
(4)

Ts is the sampling time. As illustrated in the diagram, the voltage V* can be broken down into two component vectors: VA and VB. We can write Sector A, Region 2: based on the geometry. (4)

$$\frac{V_b}{\sin \theta} = \frac{V^*}{\sin 150} = 2 * V^*$$
(5)
$$\frac{V_2}{\sin(120 - \theta_e)} = \frac{V^*}{\sin 60} = \frac{2}{\sqrt{3}} * V^*$$
(6)
$$V_A = V^* * \cos \theta_{e-} V_B \cos 30$$
(7)
Substituting (1) and (2) in (3) and simplifying, we get
$$V_A = V_2 \frac{\sqrt{3}}{\sqrt{3}} \frac{\cos \theta_e - 3 \sin \theta_e}{\cos \theta_e + 3 \sin \theta_e}$$
(8)

since

(9)

$$\frac{Tc}{0.5Ts} = \frac{V_A}{V_2}$$

We can write
$$Tc = \frac{Ts}{2} * \frac{\sqrt{3}\cos\theta_e - 3\sin\theta_e}{\sqrt{3}\cos\theta_e + 3\sin\theta_e}$$

Therefore, to establish, the vector has to be impressed for the time segment

$$Tb = \frac{Ts}{2} - Tc \tag{10}$$

as given by (4). Similar derivations were made for region 2 and 4 in all the six sectors. The resulting expressions are summarized in the second column of Table IV[2].

Table IV: Dwell Times for in region 2 and 4 of over modulation region

		0
Sect	Time segments(region2)	Time segments (region4)
or		
	Sect	Time Sect segments(region2)

RE	EGION	STAT	ES	SWITCHING
				STATES
	1.2 17-16		6	PON-PNN
	1.4	18-1	7	PPN-PON
1	$T_c = \frac{T_s}{2} \left(\frac{\sqrt{3}}{\sqrt{3}} \right)$	$\cos\theta_{g} - 3\sin\theta_{g}$ $\cos\theta_{g} + \sin\theta_{g}$	$T_b = \frac{T_s}{2}$	$\left(\frac{2\sqrt{3}\cos\theta_{e}-2\sin\theta_{e}}{\sqrt{3}\cos\theta_{s}+\sin\theta_{s}}\right)$
	$T_b = \frac{1}{2}$	$\frac{s}{2} - T_c$	7	$T_a = \frac{T_s}{2} - T_b$
2	$T_a = \frac{T_s}{2} \left(\frac{V_s}{T_s} \right)$	$\left(\frac{\sqrt{3}\cos\theta_{e}}{\sin\theta_{e}}\right)$	$T_b = \frac{T_s}{2}$	$\left(\frac{\sin\theta_{e} + \sqrt{3}\sin\theta_{e}}{\sin\theta_{e}}\right)$
	$T_b = \frac{1}{2}$	$\frac{s}{2} - T_a$	7	$T_c = \frac{T_s}{2} - T_b$
3	$T_c = \frac{T_s}{2} \left(\frac{\sqrt{3}}{\sin t} \right)$ $T_c = \frac{T_s}{2} \left(\frac{\sqrt{3}}{\sin t} \right)$	$\cos\theta_{e} + 3\sin\theta_{e}$ $d\theta_{e} - \sqrt{3}\cos\theta_{e}$ $\frac{s}{2} - T$	$T_b = \frac{T_s}{2}$	$\left(\frac{4\sin\theta_{e}}{\sin\theta_{s} - \sqrt{3}\cos\theta_{s}}\right)$ $T = \frac{T_{s}}{T_{s}} - T.$
	- 6 2	2 2		^a 2 ^b
4	$T_a = \frac{T_s}{2} \left(\frac{\sqrt{3}}{\sqrt{3}} \right)$	$\cos\theta_{e} + 3\sin\theta$ $\cos\theta_{s} + \sin\theta_{s}$	$T_b = \frac{T_s}{2}$	$\left(\frac{2\sqrt{3}\cos\theta_{e}-2\sin\theta_{e}}{\sqrt{3}\cos\theta_{s}+\sin\theta_{e}}\right)$
	$T_b = -\frac{1}{2}$	$\frac{1}{2} - T_a$	1	$T_a = \frac{1}{2} - T_b$
5	$T_c = \frac{T_s}{2} \left(\frac{V_s}{T_s} \right)$	$\left(\frac{\sqrt{3}\cos\theta_{e}}{\sin\theta_{e}}\right)$	$T_b = \frac{T_s}{2}$	$\left(\frac{\sin\theta_e + \sqrt{3}\sin\theta_e}{\sin\theta_e}\right)$
	$T_b = -\frac{1}{2}$	$\frac{s}{2} - T_c$	7	$T_a = \frac{T_s}{2} - T_b$
6	$T_a = \frac{T_s}{2} \left(\frac{\sqrt{3}}{\sin \eta} \right)$	$\cos\theta_e + 3\sin\theta_e$ $\sin\theta_e - \sqrt{3}\cos\theta_e$	$T_b = \frac{T_s}{2}$	$\begin{pmatrix} \frac{4\sin\theta_s}{\sin\theta_s - \sqrt{3}\cos\theta_s} \\ T_s \end{pmatrix}$
0	$T_b = \frac{1}{2}$	$\frac{s}{2} - T_a$	7	$T_c = \frac{T_s}{2} - T_b$

IV. PROPOSED SIMPLIFIED SVM FOR OVERMODULATION REGION

Simplified SVPWM is given in section II for under modulation region. In the proposed method, it is applied to over modulation region. Fig. 8 shows the timing diagram for over modulation, mode II. Switching pattern for region 2 and 4 of sector A is given in Table V. From Fig. 8 switching times for each switch of region 2 and 4 are calculated and is given in Table VI



Fig.8. Timing diagram for over modulation mode II

Table VSWITCHING PATTERN FOR SECTOR A

Table VI TURN ON TIMES OF ALL SWITCHES FOR REGION 5 AND 6

Region	2	4
S _{A1}	Ts/2	Ts/2
\mathbf{S}_{A2}	Ts/2	Ts/2
S _{B1}	0	Та
S _{B2}	Tb	Ts/2
S _{C1}	0	0
S _{C2}	0	0

Once turn on time is calculated for sector A, turn on times for remaining sectors are calculated using mapping method given in section II.

In a similar way turn times are also calculated for over modulation region mode I. These turn times are compared with carrier wave to generate switching pulses for three level inverter. *R. Linga Swamy, et. al. International Journal of Engineering Research and Applications www.ijera.com ISSN: 2248-9622, Vol. 11, Issue 12, (Series-I) December 2021, pp. 25-32*

C . Implementation of simplified SVPWM for 3-level NPC inverter fed induction motor

Induction machine model taken from MATLAB and simplified SVPWM is tested with simulation. The machine parameters details are given in table VII. Fig.9 shows the Simulink model for simplified space vector PWM for 3-PWM for 3-level inverter fed induction motor in both overmodulation and under modulation regions. In this model the complete operation of space vector PWM in both regions takes place. Fig.10 shows the Simulink model of the gating signal developed for 3-level inverter. MATLAB program to generate switching signal for Space vector PWM of 3-level inverter fed induction motor for over modulation region Mode I and II is developed using code written in MATLAB function block then parameters like line voltages, phase voltages, torque, speed are compared in all modes of operation.

Table VII: SPECIFIACTIONS OF INDUCTION

WIOTOR			
parameter	Value		
Rated voltage	200v		
Rated frequency	50HZ		
Rated power	1545W		
poles	4		



Fig.9. Three-level inverter connected to load



ing. 10. control circuit

V. SIMULATION RESULTS

In this section simulation results are given in under modulation and over modulation regions (Mode I and II).

Fig 11, 14 and 17 shows the Electrical torque and speed at load torque at 10.32N-m in under modulation and over modulation regions (Mode I and II) respectively. Fig 12, 15 and 18 shows Machine line voltages Vab, Vbc, Vca. in under modulation and over modulation regions (Mode I and II) respectively. Fig 13,16 and 19 shows the Machine Phase currents Ia, Ib, Ic. . in under modulation and over modulation regions (Mode I and II) respectively.



Fig 11 Electrical Torque and Speed. (Vs) Time (vs) time in under modulation region



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I _{rms}	6.352A	6.69A	6.808A
THD % of	13.05	19.01	19.11
current			
Torque	10.32	11.04	11.34

VI. CONCLUSION

A new simplified Space Vector PWM Algorithm is proposed and Verified by Simulation of a space vector PWM of three level inverter fed induction motor drive operation in Over modulation region and under modulation region. Speed, Vrms, Irms is gradually increased from under modulation to over modulation region II. So, it is useful in applications particularly where large speeds are required instantly.

Vrms increases by nearly 15% from under modulation region to over modulation region II, where as 2% in over modulation region I. Speed increases by 21RPM from under modulation region to over modulation region II, Where as 10RPM in under modulation region. Irms increases by 0.5A from under modulation region to over modulation region II, where as 0.3A in over modulation region I

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